

INDEX

PAGE	DESCRIPTION
01	Index
02	TRIP connector, USB To UART, EEPROM
03	RS-232 / RS-485
04	SW / GPIO
04	

BOARD REVISION HISTORY

BOARD REVISION: 1.0

- INITIAL REVISION

BOARD REVISION: 1.1

- Config Switch routing Fix
- Termination net naming fix
- Added 485 half / full switch to I2C extender
- Changed pinout of I2C extender
- Routed RE#, DE pin of SP330 Chips to Dedicated controll GPIO
- Changed pinout of J1,J2,J3 Connector
- Added USB connector for optionally connecting using USB
- Added LDO to 3.3v in case connected by USB cable

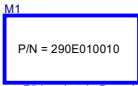
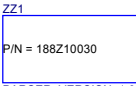
BOARD REVISION: 1.1.1

- Updated DB9 connector footprint

FT.H-SER4

REV. 1.1

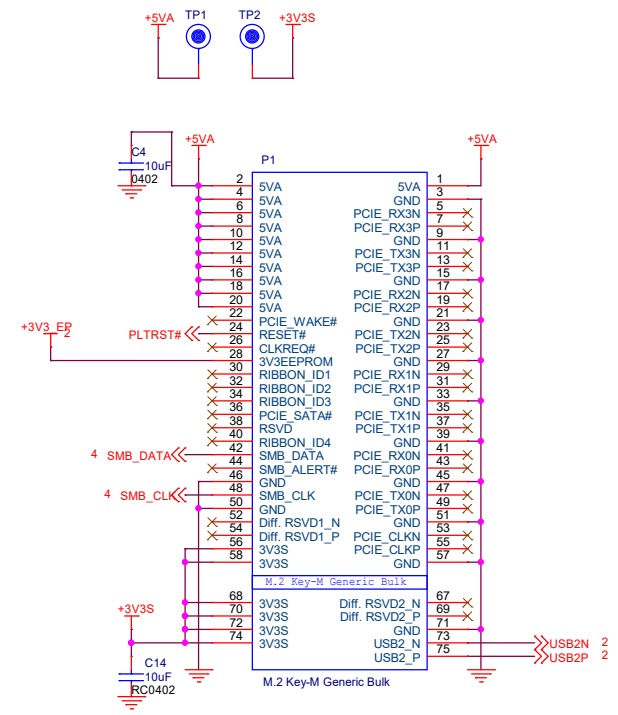
BOM ITEMS, NOT IN PCB



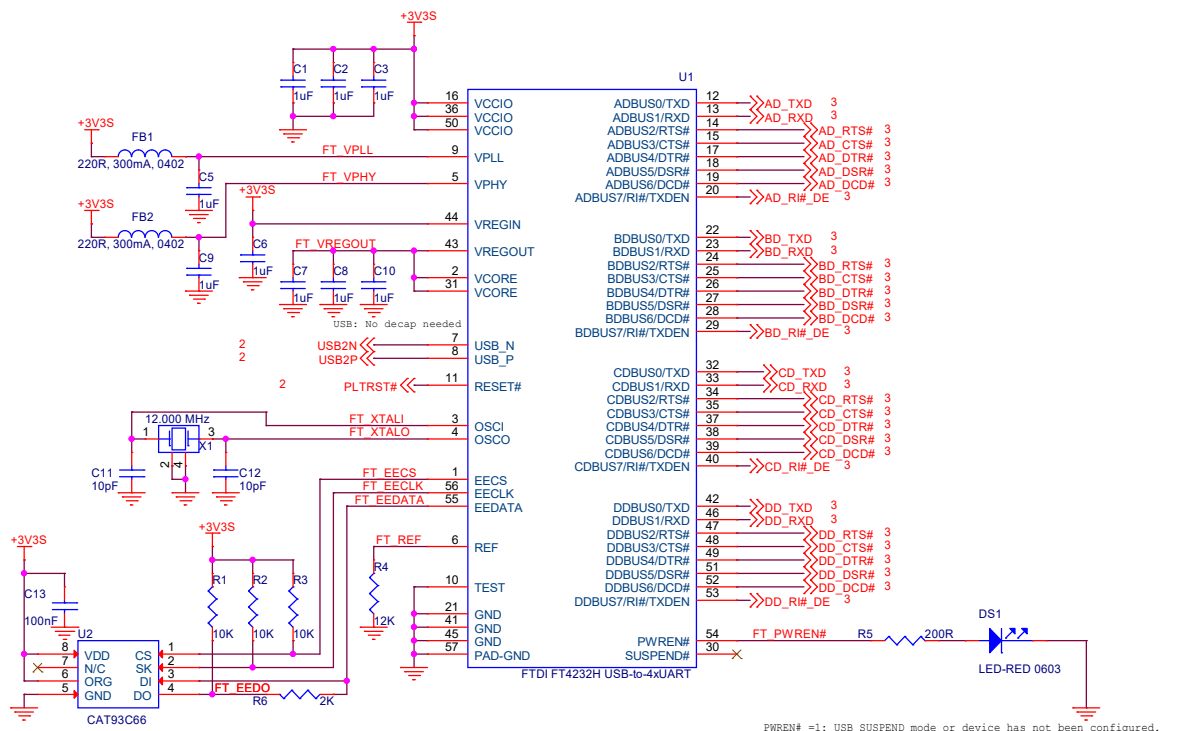
CompuLab Ltd. (972) 4 8238567
 17 HaYetsira Street
 Yokneam Illit, Israel 2069208
 All Right reserved.
 Unauthorized duplication prohibited

Size	Title	Rev
A3	INDEX	1.1
Document Number: <Doc>		
Date:	Tuesday, December 22, 2020	Sheet 1 of 4

USB TRIP

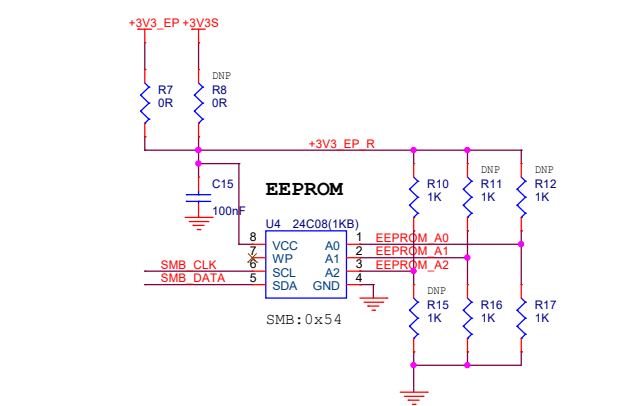


USB to 4xUART

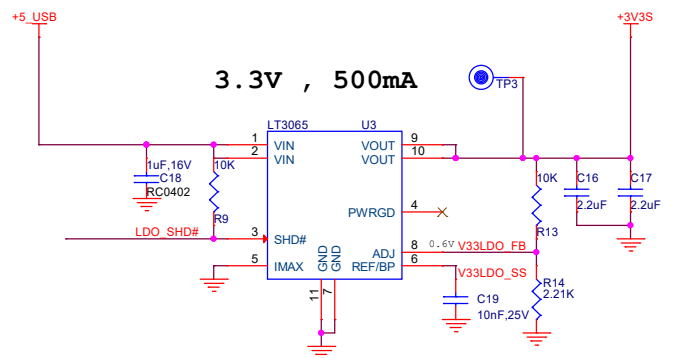


PWREN# =1: USB SUSPEND mode or device has not been configured. This can be used by external circuitry to power down logic when device is in USB suspend or has not been configured.

I2C EEPROM

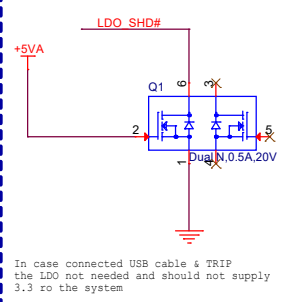


5V TO 3.3V



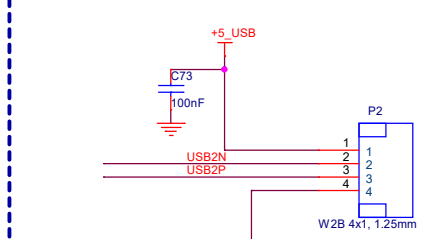
Using +5V as a source or optionally source the tel via USB connector

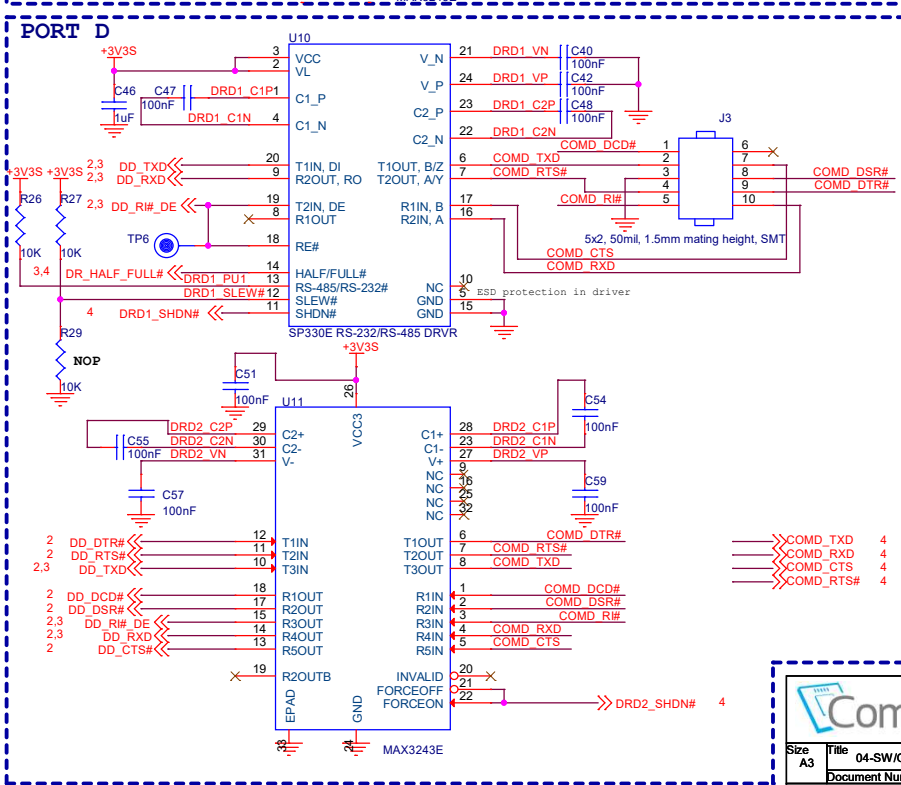
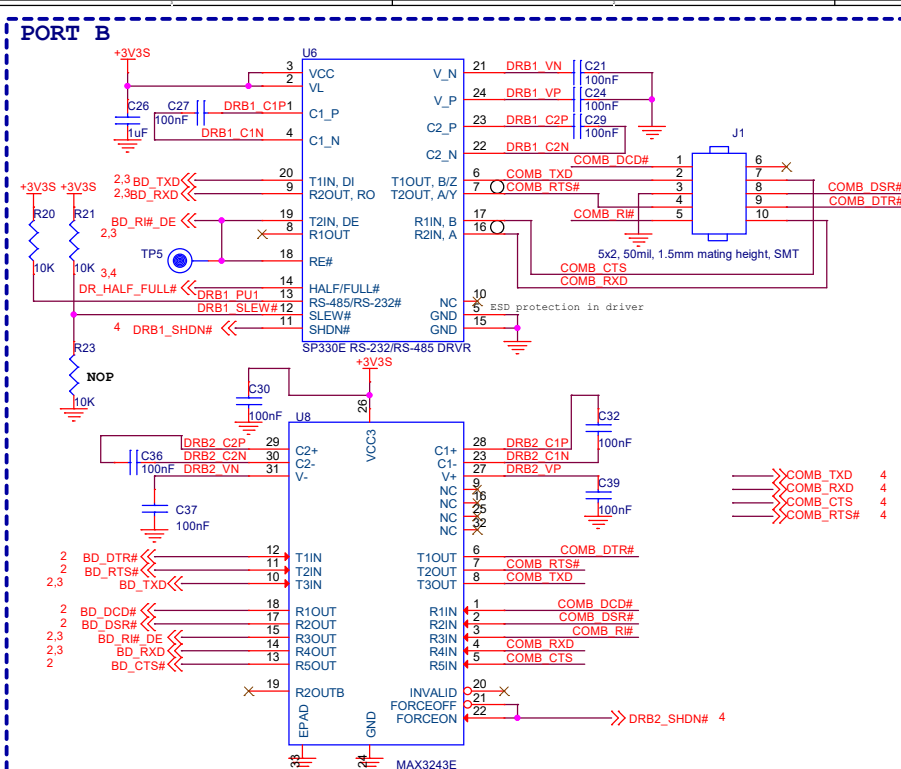
LDO CONTROLL



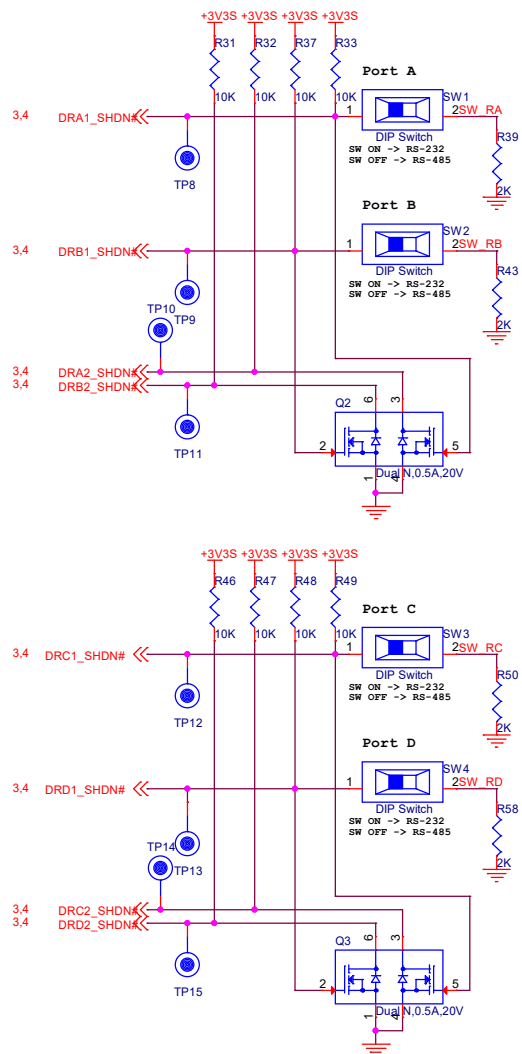
In case connected USB cable & TRIP the LDO not needed and should not supply 3.3 to the system

USB CONNECTOR

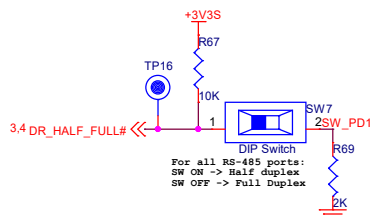




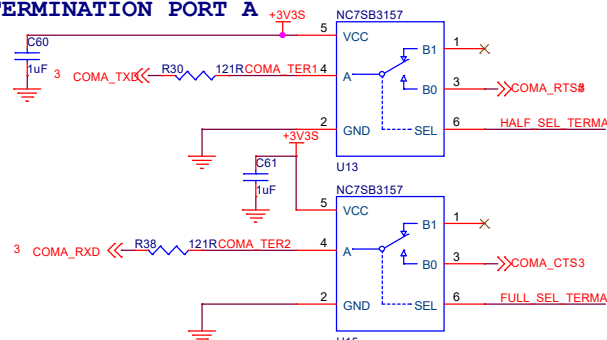
CONFIG SWITCHES PORTS A <-> D RS-232/RS-485 Switches



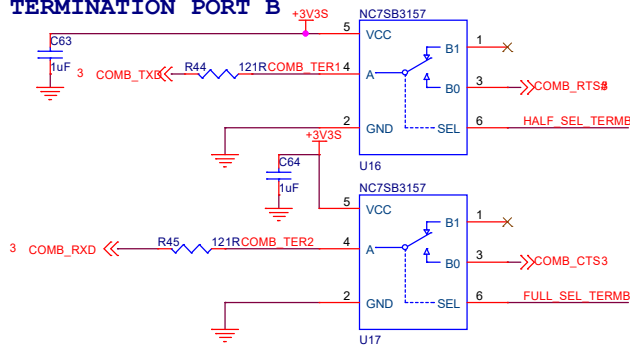
Full/Half Duplex Switch



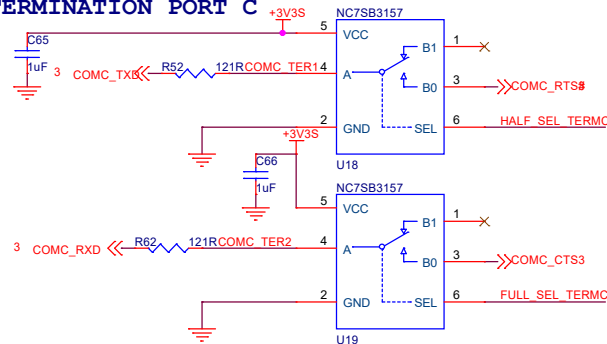
TERMINATION PORT A



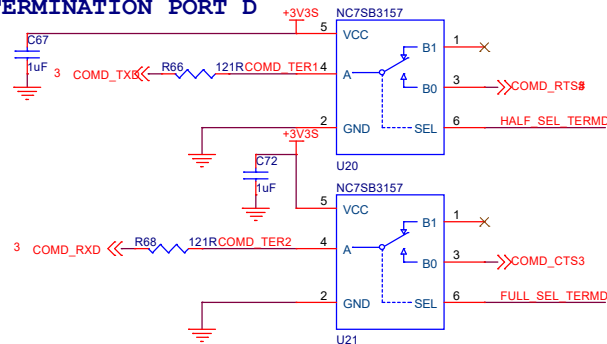
TERMINATION PORT B



TERMINATION PORT C

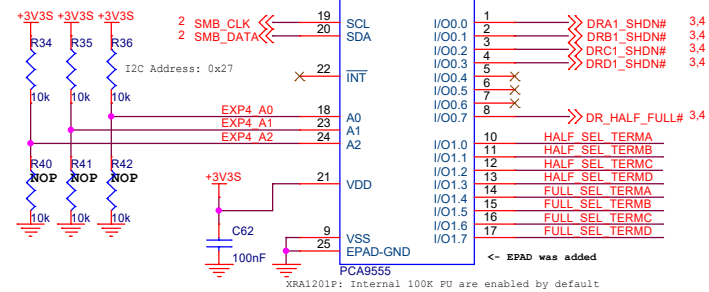


TERMINATION PORT D



I2C GPIO EXTENDER

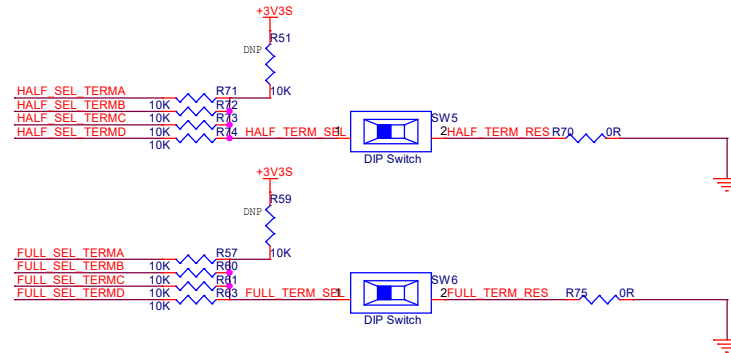
Output modes and termination can be controlled via I2C and connected PC software.



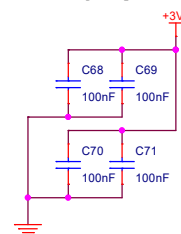
NOTE:

Port control possible only if the switches are in OFF state

TERMINATION SELECT



Stitching capacitors



Note:
The default state is RS-485 half-duplex, with termination disconnected, SW7 is controlling Half / Full 485 state.
SW1, SW2, SW3, SW4 - controlling each port for RS-232 or RS-485 individually.
SW5 - Controls Half-duplex termination of 120R (only one differential)
SW6 - Controls Full duplex termination of 120R (only one differential)